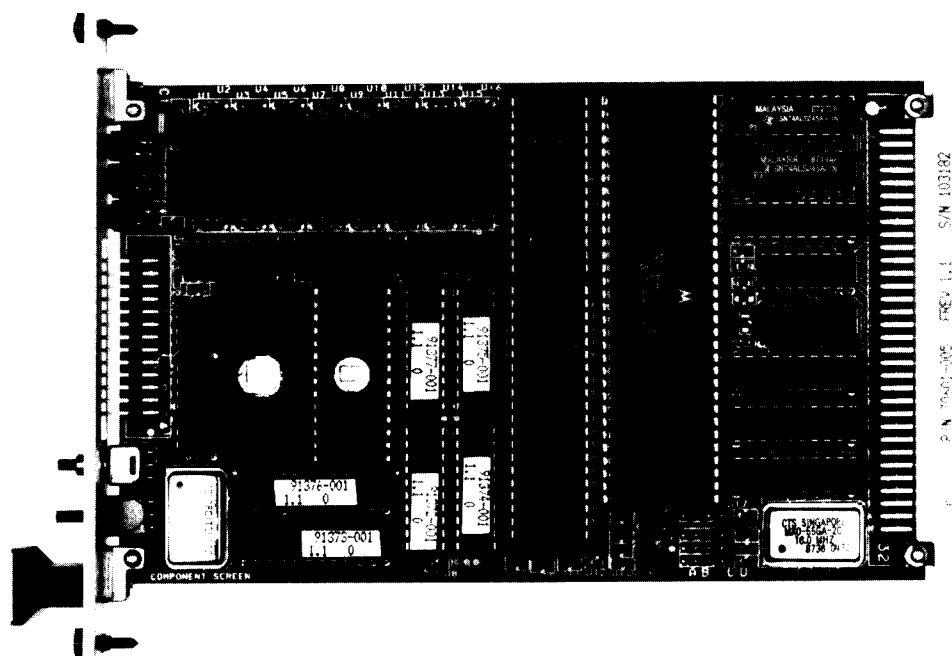




XVME-601

68000-68010 Processor Module



Features

- 10 MHz 68000 or 68010 processor
- Up to 128 Kbytes of local EPROM
- 512 Kbytes of dynamic RAM
- Two RS-232C serial ports
- 16-bit timer
- Provides all VMEbus utilities

Applications

- Process control
- Imbedded machine control
- Real-time data acquisition
- Supervisory Control and Data Acquisition (SCADA)
- Remote Terminal Units (RTU)

Overview

The XVME-601 Processor Module is a complete VMEbus-compatible, single-board processor in a single-high form factor. The module contains a 68000 or 68010 CPU running at 10 MHz with no-wait states, when executing out of local memory. Local memory consists of two EPROM sockets and 512 Kbytes of static RAM. The EPROM sockets are capable of providing a total of 128 Kbytes of EPROM.

The XVME-601 contains two RS-232C-compatible serial ports and a 16-bit timer, using a 68681 dual UART. Both channels are independently programmable and can interrupt the CPU, as can the programmable timer. The CPU can also service all seven levels of VMEbus interrupts.

In addition, the XVME-601 provides all of the system resources required to configure a functional VMEbus system. These include driving the SYSCLK and SYSRESET utilities, providing an IACK daisy-chain driver, a single-level arbiter, and a jumper-programmable bus timeout. The XVME-601 can also monitor ACFAIL, SYSFAIL, and BCLR.

Hardware Specifications

Processor	68000 or 68010
Speed	10 MHz
Memory Capacity	
EPROM	2 sockets or up to 128 Kbytes
RAM	512 Kbytes of dynamic RAM
Serial Ports	
Number	2
Compatibility	RS-232C
Baud Rates	75-19.2 Kbaud, programmable
Signals	Port A - TxD, RxD, RTS, & CTS Port B - TxD and RxD

Time Base Controller
17.4 usec to 569 msec

Front Panel Indicators
FAIL (red), PASS (green), and HALT (red)

Front Panel Switches
RESET and ABORT

Power Requirements
+5 V @ 2.2 A max.
±12 V @ 100 mA max.

VMEbus Compliance

- A24/A16:D16/D08(E0) DTB Master
- RMW capability
- IH(1)-IH(7) D08(O) Interrupt Handler
- SGL Arbiter
- R(0-3) Bus Requester
- RWD, ROR, or BOBC (STAT) bus release
- ROACF (software controlled) bus release
- BTO (16,32,64,128) STAT
- IDCD IACK Daisy Chain Driver
- SYSCLK and SYSRESET driver
- Monitors SYSFAIL, ACFAIL, and BCLR
- Form Factor: SINGLE
165.1 mm x 100.01 mm
(6.5" x 3.94")

Environmental Specifications

Temperature	
Operating	0° to 65°C (32° to 149°F)
Non-operating	-40° to 85°C (-40° to 185°F)
Humidity	5 to 95% RH, non-condensing (Extremely low humidity conditions may require special protection against static discharge.)
Altitude	
Operating	Sea level to 10,000 ft. (3048 m)
Non-operating	Sea level to 50,000 ft. (15240 m)

Vibration	
Operating	5 to 2000 Hz .015" peak-to-peak displacement 2.5 g (max.) acceleration
Non-operating	5 to 2000 Hz .030" peak-to-peak displacement 5.0 g (max.) acceleration

Shock	
Operating	30 g peak acceleration 11 msec duration
Non-operating	50 g peak acceleration 11 msec duration

Warranty Information

The XVME-601 carries a two-year warranty.

Ordering Information

XVME-601/1: Single-High VMEbus 68000 Processor
XVME-601/5: Single-High VMEbus 68010 Processor
XVME-990/2: Debug Monitor for XVME-601
XVME-943: 6U Front Panel Kit

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